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REMARKS/ARGUMENTS

The applicant is sure about the previously presented claim 1, which is described with commonly used claim language, can be understood clearly and has no grammar errors. And the content of claim 11 is totally supported by paragraph [0026] "the present invention capacitor is not only disposed in the pixel array area on the array substrate of the liquid crystal display, but also is disposed in the periphery circuit area on the array substrate of the liquid crystal display". Thus, reconsideration of claims 1 and 11 is requested.

Rejection of claims 1-21 under 35 U.S.C. 103 (a) as being anticipated by APA in view of Ikeda at al. (US 5,182,661).

In Fig. 4 of Ikeda at al. (US 5,182,661), the third storage capacitor electrode 68 corresponds to the first conductive layer of the applicant's invention. The third storage capacitor electrode 68 and the gate bus line 10 are made by the same conductive layer in Ikeda at al. (US 5,182,661) (Col 5, lines 58-60, "a third storage capacitor electrode 68 formed of chromium is provided to extend from the gate bus line 10"). So, the first conductive layer connects to a gate in Ikeda at al. (US 5,182,661). But, claim 1 of the applicant's invention recites that the first conductive layer does not connect to a gate. Thus, the structure of the applicant's invention is different from the structure of Ikeda at al. (US 5,182,661).

Furthermore, the first conductive layer connects to a gate in Ikeda at al. (US 5,182,661), and the first conductive layer doesn't connect to a gate in APA. Therefore, the structure, which is made by APA in view of Ikeda at al. (US 5,182,661), can't exist, and these two prior art references cannot be combined. Thus, claim 1 is patentably distinguishable from APA in view of Ikeda at al. (US 5,182,661). Reconsideration of the claims is politely requested.

Applicant respectfully requests that a timely Notice of Allowance be issued in this

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case.

Sincerely yours,

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Weinton taus Date: 05/19/2006

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